

REMARKS

At the outset, the Examiner is thanked for the thorough review and consideration of the pending application. The Office Action dated December 10, 2008 has been received and its contents carefully reviewed.

Claims 4, 5 and 19 are hereby amended and claims 1-3 and 24-25 are hereby cancelled. No new matter is added. Accordingly, claims 4-5, 7-23 are currently pending. Reexamination and reconsideration of the pending claims are respectfully requested.

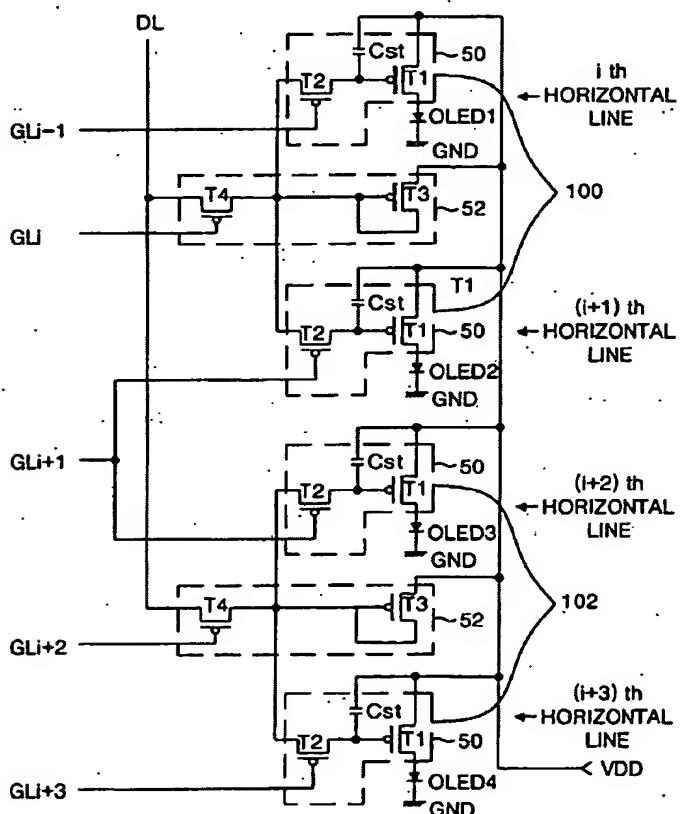
In the Office Action, claims 1-5, 7-10 and 19-25 are rejected under 35 U.S.C. 102(b) as being anticipated by Inukai (U.S. Pub. No. 2002/0000576, hereinafter referred to as "Inukai"), and claims 11-18 are rejected under 35 U.S.C. 103(a) as being unpatentable over Inukai in view of Komiya (U.S. Patent No. 6,924,602, hereinafter referred to as "Komiya").

Applicants respectfully submit that claims 4 and 19 are allowable over Inukai and Komiya and reconsideration is respectfully requested. **Claim 4** recites an electro-luminescence display device, having a combination of elements comprising, for example, "driving circuits for controlling a current applied from the driving voltage of the supply voltage line to the electro-luminescence cells in response to video signals, wherein each of driving circuit includes a first driving circuit and a second driving circuit; and control circuits for applying the video signals to the driving circuits, wherein each of the control circuits is directly connected between the data line and supply voltage line and is positioned between the first driving circuit and the second driving circuit so that the control circuit supplies the video signal to the first driving circuit and the second driving circuit", and **claim 19** recites an electro-luminescence display device, having a combination of elements comprising, for example, "driving circuits for applying a current corresponding to the video signals to the electro-luminescence cells in response to the video signals; and control circuits for applying the video signals to the driving circuits, wherein each of the control circuits is directly connected between the data line and supply voltage line and is positioned between the first driving circuit and the second driving circuit so that the control circuit supplies the video signal to the first driving circuit and the second driving circuit". Any one of Inukai and Komiya fails to describe, expressly or inherently at least these features of the claimed invention.

None of Inukai and Komiya discloses that each of the control circuits is directly connected

between the data line and supply voltage line and is positioned between the first driving circuit and the second driving circuit so that the control circuit supplies the video signal to the first driving circuit and the second driving circuit. As shown in figure 6 of the claimed invention, the control circuit (52) of the claimed invention is directly connected between the data line (DL) and supply voltage line (VDD) and is positioned between the first driving circuit (driving circuit 50 positioned at the upper side of the control circuit 52) and the second driving circuit (driving circuit 50 positioned at the below side of the control circuit 52) so that the control circuit (52) supplies the video signal to the first driving circuit (50) and the second driving circuit (50).

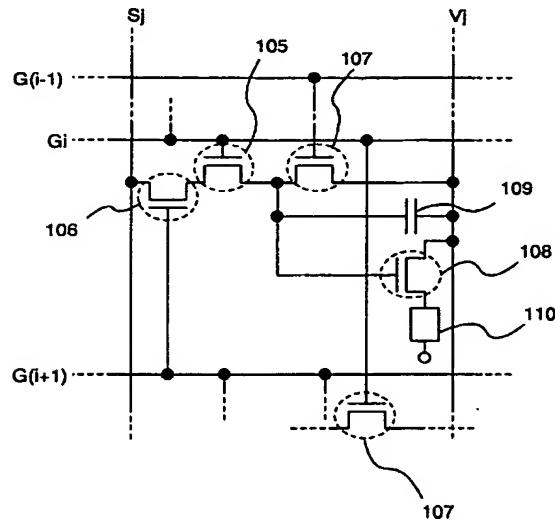
[Fig. 6 of the claimed invention]



On the other hand, Inukai discloses the pixel circuit comprising a first switching TFT (106), a second switching TFT (105), an erasing TFT (107) a driving TFT (108), a capacitor (109), and an EL element (110). In the Inukai, constructional elements directly connected between the data line (S_j) and the power line (V_j) are the first and second TFTs (106, 105) and the erasing TFT (107). However, they (105, 106, 107) cannot supply any video signal from the

data line to two driving circuit. They (105, 106, 107) can supply the video signal to one driving circuit (108) corresponding to the same horizontal line. Accordingly, Inukai fails to teach or suggest the features of the claimed invention.

[Fig. 3 of Inukai]



Also, the control circuit (TFT4) of Komiya is connected between a power source line (PVDD) and a driving TFT (TFT2). Therefore, Komiya fails to disclose the construction of the control circuit (52) of the claimed invention directly connected between the data line (DL) and supply voltage line (VDD). Furthermore, the control circuit (TFT4) of Komiya cannot supply any video signal from the data line to two driving circuit. the control circuit (TFT4) can supply the video signal to one driving circuit (TFT2) corresponding to the same horizontal line. Accordingly, Komiya fails to teach or suggest the features of the claimed invention.

[Fig. 1 of Komiya]

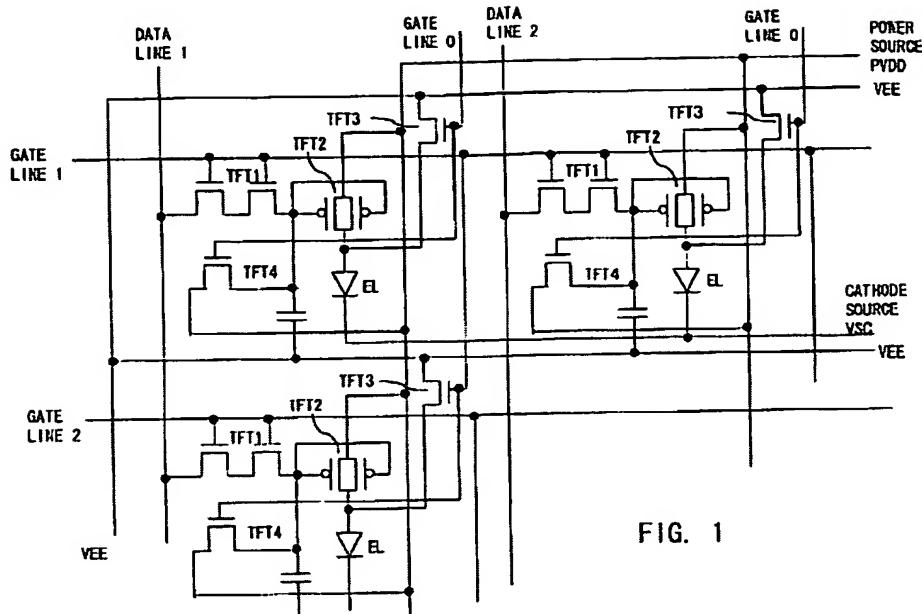


FIG. 1

Accordingly, Applicants respectfully submit that claims 4 and 19, and claims 5, 7-10 and 20-23 which depend from claim 4 or 19 are not anticipated by Inukai or Komiya, because any of Inukai and Komiya fails to teach, either expressly or inherently, at least these features of the claimed invention.

Also, Applicants respectfully submit that claims 11-18 are allowable over Inukai and Komiya because Inukai and Komiya fail to teach, either expressly or inherently, at least these features of the claimed invention.

Applicants believe the foregoing amendments and remarks place the application in condition for allowance and early, favorable action is respectfully solicited.

If for any reason the Examiner finds the application other than in condition for allowance, the Examiner is requested to call the undersigned attorney at (202) 496-7500 to discuss the steps necessary for placing the application in condition for allowance. All correspondence should continue to be sent to the below-listed address.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time, or any other fees required to complete the

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Docket No.: 8733.1031.00

filings of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911.

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Respectfully submitted,

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